

**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**

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First Named Inventor	Chilimbi
Art Unit	2192
Examiner Name	Zheng Wei

U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date and submit a copy.

Examiner's Initials*	Cite No. (optional)	Number	Date	Name of Applicant or Patentee
		6,216,219	04-2001	Cai et al.
		6,598,141	07-2003	Dussud et al.

Examiner's Initials*	Cite No. (optional)	OTHER DOCUMENTS
		Cifuentes, "Structuring Decompiled Graphs," Technical Report 5/94, Faculty of Information Technology, Queensland University of Technology, GPO Box 2434, Brisbane 4001, Australia (April 1994), 15 pages.
		Hauck, "Configuration Prefetch for Single Context Reconfigurable Coprocessors," In Proceedings of the ACM/SIGDA International Symposium on Field Programmable Gate Arrays (February 1998), pages 65-74.
		Ung et al., "Optimising Hot Paths in a Dynamic Binary Translator," In Workshop on Binary Translation, October 2000, pp. 55-65.

EXAMINER SIGNATURE:	/Zheng Wei/	DATE CONSIDERED:	06/15/2009
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* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.